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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mitsuaki Osame et al. Art Unit : 2816
Serial No. : 10/724,365 Examiner : Unknown
Filed : December 1, 2003
Title : DATA LATCH CIRCUIT AND ELECTRONIC DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the names on the filing receipt under Power of Attorney to read as follows:

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Please supply a corrected Filing Receipt to the undersigned with respect to this application. A copy of the original Filing Receipt showing the desired changes in red ink is attached for your convenience.

No fee is believed to be due. If, however, there are any charges or credits, please apply them to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 9/30/04



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/724,365	12/01/2003	2816	2280	12732-183001 / US6776	10	56	10

26171
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CONFIRMATION NO. 8069

UPDATED FILING RECEIPT



OC000000013792823

Date Mailed: 09/14/2004

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Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

✓ JAPAN 2002-351672 12/03/2002

Docketed By Practice Systems	
Action Code:	STATEMENT RE POA
Base Date:	9/14/04
Due Date:	10/14/04
Deadline:	
Initials:	A
Record:	

If Required, Foreign Filing License Granted: 04/24/2004

The number of your priority application, to be used for filing abroad under the Paris Convention is,
US10/724,365

Projected Publication Date: 12/23/2004

Docketed	
Due Date	10/14/04
Deadline	
Initials	CMT

Non-Publication Request: No

Early Publication Request: No

Title

✓ Data latch circuit and electronic device

Preliminary Class

327

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